

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS: Chatterjee et al.  
SERIAL NO.: 10/788,943  
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TITLE: Memory Unit with Controller Managing Memory Access through  
JTAG and CPU Interfaces  
EXAMINER: Kerveros, James C  
ART UNIT: 2138  
ATTY. DKT. NO.: IDT-1872

*entered per  
RCE  
10-26-07*

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MAIL STOP AMENDMENT  
COMMISSIONER FOR PATENTS  
P.O. BOX 1450  
ALEXANDRIA, VA 22313-1450

RESPONSE TO SUPPLEMENTAL OFFICE ACTION

Dear Sir:

In response to the Supplemental Final Office Action mailed on August 28, 2007, having a shortened statutory period set to expire on September 28, 2007, please consider the following remarks.

Remarks begin on page 2 of this paper.